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Certificate

DATE:

August 11, 2005

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FILE NO:

INFN/SZ0030

of Correction

TO:

Certificate of Correction Branch

Commissioner for Patents

FAX NO:

1-571-273-8300

FROM:

Randol W. Read

PAGE(S) with cover:

ORIGINAL TO FOLLOW?

☐ YES ⊠ NO

RE:

PATENT NO.:

US 6,888,767 B1

ISSUE DATE:

May 3, 2005

TITLE:

DUAL POWER SENSING SCHEME FOR A MEMORY

DEVICE

U.S. SERIAL NO.:

10/723,211

FILING DATE:

November 26, 2003

INVENTOR(S):

Jonghee Han

EXAMINER:

Son T. Dinh

GROUP ART UNIT:

2818

CONFIRMATION NO.:

8517

Attached are the following document(s) for the above-referenced application / issued patent:

- 1. Request for Certificate of Correction
- Certificate of Correction (form SB/44) 2.

CONFIDENTIALITY NOTE

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AUG 1 1 2005

PATENT SZS&Z Ref. No.: IO031112PUS Atty. Dkt. No. INFN/SZ0030

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jonghee Han

Serial No.: 10/723,211

Filed:

November 26, 2003

For:

DUAL POWER SENSING

SCHEME FOR A MEMORY DEVICE

<u>aaaaaaaaa</u> Group Art Unit: 2818

Examiner: Son T. Dinh

Patent No.: US 6,888,767 B1

Issued:

May 3, 2005

Certificate of Correction Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF MAILING OR TRANSMISSION

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y M.

August 11, 2005

Randol W. Read

Dear Sir:

REQUEST FOR CERTIFICATE OF CORRECTION

Attached is a Certificate of Correction for correcting several errors throughout the printed patent.

Applicant submits that the errors mentioned above were not by the applicant, but were made during the printing of the patent.

Respectfully submitted,

Randol W. Read

Registration No. 43,876

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PTO/SB/44 (04-05)
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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.: US 6,888,767 B1

Page <u>1</u> of <u>1</u>

APPLICATION NO.: 10/723,211

ISSUE DATE: May 3, 2005

INVENTOR(S): Jonghee Han

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 1, Lines 61-62, replace "VH_BLSA1/VL_BLSA1 and VH_BLSA2N/L_BLSA2" with --VH_BLSA1/VL_BLSA1 and VH_BLSA2/VL_BLSA2--.

In Column 2, Line 28, replace "VL BLSA1" with --VL_BLSA1--; Line 31, replace "VINT_M" with --V_{INT_M}--; Line 32, replace "V_{INT}M" with --V_{INT_M}--.

In Column 4, Line 41, replace "BLSAS" with --BLSAs--.

In Column 5, Line 34, " Φ ACTIVE₂" with -- Φ _{ACTIVE2}--; Line 61, replace "switches ²²²" with --switches 222--.

In Column 6, Line 3, replace "VINT" with -- V_{INT} --; Line 7, replace "VINT— $_{M1}$ and VINT— $_{M2}$ " with -- V_{INT} — $_{M1}$ and V_{INT} — $_{M2}$ --; Line 13, replace "VINT— $_{M1}$ and V_{INT} M2" with -- V_{INT} — $_{M2}$ --; Line 26, replace " Φ PRE CHARGE" with -- Φ PRE CHARGE--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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